



THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

DeWitt, Jr. et al.

Serial No: **10/675,751**

Filed: **September 30, 2003**

Title: **Method and Apparatus for
Monitoring Processing of Instructions and
Memory Locations in a Data Processing
System**

Group Art Unit: **2183**

Intellectual Property
Law Department
International Business
Machines Corporation
11400 Burnet Road
Austin, Texas 78758

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants submit herewith patents, publications or other information of which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. 1.56.

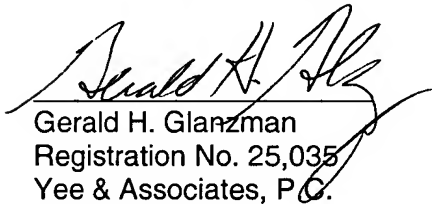
While this Supplemental Information Disclosure Statement may be "material" pursuant to 37 C.F.R. 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

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No item of information cited in this Supplemental Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in this Supplemental Information Disclosure Statement was known to any individual designated in § 1.56(c) more than three months prior to the filing of this Supplemental Information Disclosure Statement.

In the event a fee is required in connection with the enclosed Supplemental Information Disclosure Statement, the Commissioner of Patents and Trademarks is authorized to charge Deposit Account No. 09-0447 for the necessary amount.

Respectfully submitted,



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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT****Complete if Known**

Application Number	10/675,751
Filing Date	09/30/2003
First Named Inventor	DeWitt, Jr. et al.
Examiner Name	Cody, Dillon J.
Art Unit	2183
Attorney Docket No.	AUS920030482US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
AA	5,710,881	01-20-1998	Gupta et al.	
AB	5,822,763	10-13-1998	Baylor et al.	
AC	5,928,334	07-27-1999	Mandyam et al.	
AD	6,094,709	07-25-2000	Baylor et al.	
AE	6,285,974 B1	09-04-2001	Mandyam et al.	
AF	6,636,950 B1	10-21-2003	Mithal et al.	
AG	2004/0205302 A1	10-14-2004	Cantrill	
AH	5,797,019	08-18-1998	Levine et al.	
AI	6,549,998 B1	04-15-2003	Pekarich et al.	
AJ	2003/0101367 A1	05-29-2003	Bartfai et al.	
AK	5,113,507	05-12-1992	Jaeckel	
AL	6,681,387 B1	01-20-2004	Hwu et al.	
AM	6,925,424 B2	08-02-2005	Jones et al.	

FOREIGN PATENT DOCUMENTS

Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

NON PATENT LITERATURE

Examiner Initial	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
AN	TANENBAUM, "Structured Computer Organization", 1984, Prentice-Hall, Inc., 2 nd Edition, pp. 10-12.
AO	TORRELLAS et al., "False Sharing and Spatial Locality in Multiprocessor Caches", June 1994, IEEE Transactions on Computers, Vol. 43, No. 6, pp. 651-663.
AP	ROTHMAN et al., "Analysis of Shared Memory Misses and Reference Patterns", 2000, IEEE, pp. 187-198.
AQ	Wikipedia "JavaServer Pages" downloaded 1/24/06 http://en.wikipedia.org/wiki/JavaServer_Pages
AR	"Hardware Cycle Based memory Residency", IBM, May 22, 2003, ip.com, IPCOM000012728D, pp. 1-2
AS	HYDE, "The Art of Assembly Language", 2001, Linux Edition, pp. 247-248, retrieved Mar. 1, 2005 from http://webster.cs.ucr.edu/AoA/Linux/PDFs/0_PDFIndexLinux.html

Examiner:

Date Considered:

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.